

Fig. 2

- 101, 201, 301 -
 first cell disassembling section
 102, 202, 302 -
 second cell disassembling section
 103, 203, 303 - decoder
 104, 204, 304 - multiplexer
 105, 205, 305 - signal generator
 106, 206, 306 - address indicator
 107, 207, 307 - controller
 108, 208, 308 - detector
 109, 209, 309 -
 second cell assembling section
 110, 210, 310 - selector
 111, 211, 311 - coder
 112, 212, 312 -
 first cell assembling section

